

# (12) UK Patent Application (19) GB (11) 2 366 424 (13) A

(43) Date of A Publication 08.03.2002

(21) Application No 0108066.2

(22) Date of Filing 30.03.2001

(30) Priority Data

(31) 09540825

(32) 31.03.2000

(33) US

(71) Applicant(s)

EMC Corporation  
(Incorporated in USA - Massachusetts)  
35 Parkwood Drive, Hopkinton, MA 01748,  
United States of America

(72) Inventor(s)

Yuval Ofek  
David L Black  
Stephen D Macarthur  
Richard Wheeler  
Robert Thibault

 (51) INT CL<sup>7</sup>

G06F 13/36

(52) UK CL (Edition T)

G4A AFGDC

(56) Documents Cited

GB 2360377 A

(58) Field of Search

UK CL (Edition S) G4A AFGDC AMC  
Online databases: WPI, EPODOC, JAP10, INSPEC

(74) Agent and/or Address for Service

Reddie & Grose  
16 Theobalds Road, LONDON, WC1X 8PL,  
United Kingdom

(54) Abstract Title

Data transfer via director boards and a cache

(57) A system interface includes a plurality of first directors 180 and a plurality of second directors 200 divided amongst a plurality of first and second director boards 190, 210, each having a crossbar switch 320 coupling the directors input/output ports on one of a pair of message network boards, the interface further including a data transfer section and a message network. The data transfer section includes a cache memory 320' which is coupled to the directors 180, 200. The messaging network operates independently of the data transfer section and is also coupled to the first and second director boards 190, 210. Data transfer between the first directors and the second directors is controlled in response to messages passing between them through the messaging network 260 and the data passes through the cache memory 320' in the data transfer section.

The first and second directors may be coupled respectively to computer/server 120 and a bank of disc drives 141.

